REMARKS

Reconsideration of the application in view of the above amendments and the following remarks is respectfully requested.

Amendment to the Drawings

Applicants amended the drawings to include a "Prior Art" designation for FIGS. 1a and 1b. A replacement sheet is submitted herein.

Rejection of the Claims Under 35 U.S.C. § 102

In the Office Action dated November 29, 2004, claims 1-2 were rejected under 35 U.S.C. § 102(b) as being unpatentable over US Patent 6,256,495, issued to Francisco et al. (hereafter referred to as "*Francisco*").

Amended claim 1 recites:

1. A tunable discrete LC filter comprising:

an input for receiving an input signal for processing, said input signal comprising a plurality of frequencies;

control input for receiving information to select at least one band of frequencies for processing;

first inductor bank for filtering a first band of frequencies;

second inductor bank for filtering a second band of frequencies; and

switch circuit comprising a switching element and an isolation element, coupling said input signal to said first inductor bank or said second inductor bank,

said switching element comprising a series resistance and said isolation element for isolating said series resistance from said first inductor bank or said second inductor bank so as to enhance a Q factor for said LC filter, said switch circuit for selecting said first inductor bank if said first band of frequencies is selected, and for selecting said second inductor bank if said second band of frequencies is selected.

Claim 1 has been amended to include that the switch circuit comprises "*a switching element and an isolation element.*" The switching element comprises a "*a series resistance.*" The isolation element for "*isolating said series resistance from said first inductor bank and said second inductor bank so as to enhance a Q factor for said LC filter.*" Independent claims 2 and 9 include similar limitations.

Francisco discloses a multi-port transmission circuit. A semiconductor switching leg, consisting of a capacitor and a diode, is used to link ports of the multi-port circuit. The diodes are biased in the switching legs through use of a DC biasing signal, through an inlet and outlet inductor. The diodes introduce a series resistance to the inductors (*i.e.*, diode D_1 introduces a series resistance to inductors L_{21} and L_{11}). The multi-port circuit of *Francisco* does not contain an isolation element to isolate the series resistance, introduced by the diode, from the inductors. As such, *Francisco* does not anticipate the claimed invention because *Francisco* fails to teach toward or disclose a switch circuit that isolates a series resistance from said inductor banks so as to enhance a Q factor for an LC filter, as claimed.

CONCLUSION

In view of the foregoing, it is submitted that the claims are in condition for allowance. Reconsideration of the rejections and objections is requested. Allowance is earnestly solicited at the earliest possible date.

Respectfully submitted,

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Dated: April 1, 2005

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